



Bideford College Technology Faculty: Electronics Key Stage 4 Progress Matrix

| In order to get a grade G at GCSE you must be able to: | In order to get an E-F Grade at GCSE you must be able to | In order to get a C-D grade at GCSE you must be able to | In order to get a B-A grade at GCSE you must be able to | In order to get a A* grade at GCSE you must be able to |
|---|---|--|--|--|
| E1.1: recall at least 5 circuit symbols and explain the following processes; Transistors, thyristors. | E12.1: recall 10 circuit symbols and explain the following processes; transistors, thyristors, astable. | E13.1: Recall a wide range of components and processes including; monostable, logic gates, drivers. | E14.1: recall and explain in detail the complete range of components and processes studied. | E15.1: recall and explain in detail the complete range of components and processes studied. |
| E1.2: copy simple flowchart solutions. | E12.2: produce simple digital flowchart solutions with guidance. | E13.2: produce independently simple digital and analogue flowchart solutions to a given problem. | E14.2: use formulae with confidence to calculate component and circuit values. | E15.2: use all formulae with confidence to calculate component and circuit values without error. |
| E1.3: use correct units to describe component values (ohms, voltage, amps). | E12.3: use formulae to calculate component values (current limiting resistors). | E13.3: use formulae to calculate component and circuit values for monostable and astable circuits. | E14.3: use knowledge of electronic systems to generate complex ideas (2 or more processes). | E15.3: use knowledge of electronic systems to generate complex ideas (3 or more processes) |
| E1.4: generate ideas with guidance. | E12.4: use knowledge of electronic systems to generate ideas with guidance. | E13.4: use knowledge of electronic systems to generate ideas with 1 process. | E14.4: produce circuit diagrams, PCB layouts and flowcharts without errors. | E15.4: produce circuit diagrams, PCB layouts and flowcharts quickly and without errors. |
| E1.5: test given circuits using computer simulation. | E12.5: produce circuit diagrams, PCB layouts and flowcharts with guidance. | E13.5: produce circuit diagrams, PCB layouts and flowcharts with occasional errors. | E14.5: test circuits using computer simulation and breadboarding independently. | E15.5: test and calibrate circuits using computer simulation and breadboarding independently. |
| E1.6: solder circuits with support. | E12.6: test circuits using computer simulation. | E13.6: test circuits using computer simulation and breadboarding. | E14.6: solder circuits with accuracy with no evidence or risk of dry joints and/or short circuits. | E15.6: solder circuits without error. |
| E1.7: present coursework sheets are with a title. | E12.7: solder circuits and correct errors when required. | E13.7: solder circuits with accuracy insulating components where appropriate. | E14.7: ensure coursework sheets are coherent and developed in terms of research and circuit development. | E15.7: ensure All coursework sheets are completed in detail and decisions are fully explained. |
| | E12.8: present coursework sheets tidily and in a consistent format. | E13.8: present coursework sheets (research and circuit development) which are annotated and explained. | | |

